

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

T. WATANABE et al.

Serial No.:

09/462,796

Filed:

January 13, 2000

For:

METHOD FOR PRODUCING A SEMICONDUCTOR DEVICE WITH PYRAMIDAL BUMP ELECTRODES BONDED ON PAD ELECTRODES ARRANGED ON A SEMICONDUCTOR CHIP

(As Amended)

Group:

2813

Examiner:

T. Nguyen

## SUBMISSION OF REPLACEMENT DRAWINGS

**Mail Drawings** 

May 9, 2008

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please substitute the enclosed sheets for the originally-filed Figs. 3, 6, 8(i) and 9(i) drawings. Figs. 3, 6, 8(i) and 9(i) contain corrections approved by the Examiner in the Office Action dated May 20, 2002. None of the other sheets of drawings requires correction.

No fee is believed to be due for this submission. However, please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, or credit any overpayment of fees, to the deposit account of Mattingly, Stanger, Malur & Brundidge, P.C., Deposit Account No. 50-1417 (referencing Attorney Docket No. 500.38090X00).

Respectfully submitted,

MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.

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